

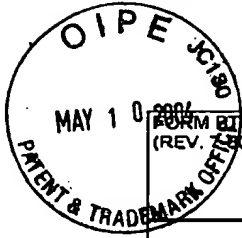
MAY-10-04

02:38PM

FROM- MATTINGLY, STANGER &amp; MALUR, P.C.

+ 703 684 1157

T-089 P.011/014 F-888



Sheet 1 of 1

FORM PTO-1449 (REV. 7-00)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. ASA-904		SERIAL NO. 09/622,372		
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT T. SEKIGUCHI et al				
				FILING DATE August 16, 2000		GROUP 2184		
<b>U.S. PATENT DOCUMENTS</b>								
EXAMINER INITIAL		DOCUMENT	DATE	NAME	CLASS	SUBCLASS	FILING DATE (If Appropriate)	
	AA							
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
<b>FOREIGN PATENT DOCUMENTS</b>								
		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
MM	AL	9-50386	02/18/97	Japan			<input type="checkbox"/>	<input type="checkbox"/>
MM	AM	5-257914	10/08/93	Japan			<input type="checkbox"/>	<input type="checkbox"/>
MM	AN	5-250284	09/28/93	Japan			<input type="checkbox"/>	<input type="checkbox"/>
MM	AO	1-292553	11/24/89	Japan			<input type="checkbox"/>	<input type="checkbox"/>
	AP						<input type="checkbox"/>	<input type="checkbox"/>
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)</b>								
MM	AR	PCI HARDWARE AND SOFTWARE ARCHITECTURE DESIGN, Annabooks, 1994, pp. 172-174.						
MM	AS	MICROPROCESSOR REPORT, Vol. 12, No. 9, July 13, 1998, pp. 6-12.						
	AT							
EXAMINER <i>Michael Moshulinski</i>				DATE CONSIDERED <i>7/19/04</i>				